

NMOS noisy nullor circuit

Models a nullor with NMOS input-referred noise sources

NMOS parameters: W, L, M, ID, VGS, VDS ($I_D > 0$, $V_{GS} > 0$, $V_{DS} > 0$)

Put the library directive in the top-level circuit

A1

```
.subckt noisyNullorN outP outN inP inN W=220n L=180n M=1 ID=1u VGS=0.6 VDS=0.9
```

Noisy nullor

