

PMOS noisy nullor circuit

Models a nullor with PMOS input-referred noise sources
PMOS parameters: W, L, M, ID, VGS, VDS ($ID > 0$, $VGS < 0$, $VDS < 0$)
Put the library directive in the top-level circuit

A1

```
.subckt noisyNullorP outP outN inP inN W=220n L=180n M=1 ID=1u VGS=0 VDS=-0.9
```

Noisy nullor

