

# Amplifiers, concept, types and ideal behavioral models

## Definition

### Amplifier object

- Three electrical ports
- input port: connection to signal source
- output port: connection to load
- power port: connection to power supply

### Amplification function

- provide load with accurate copy of source signal

### Characteristic property

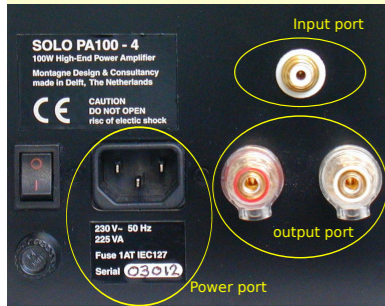
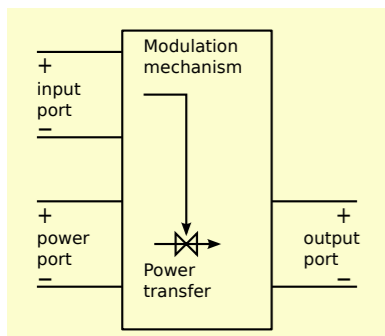
- Available power gain exceeds unity

### Functional model

- Two-port input and output port only
- Active (delivers power)
- Linear, instantaneous, and time-invariant:

$$y(t) = Ax(t)$$

$$A = \text{constant}$$



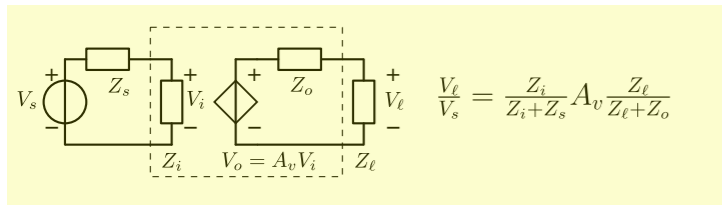
## Example voltage amplifier

### Source

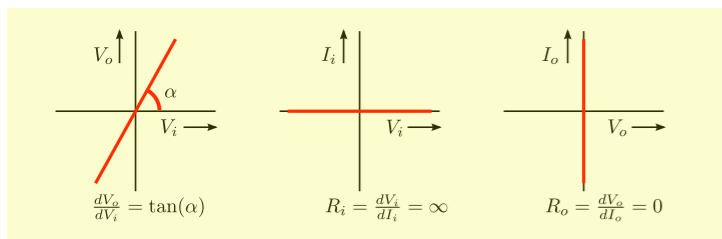
- Information accurately related to open-circuit voltage
- Source impedance inaccurately known

### Load

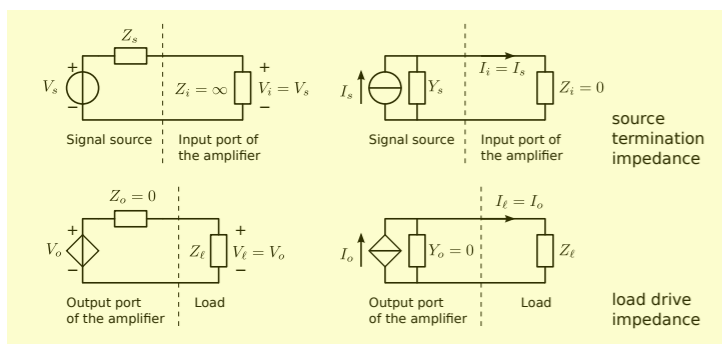
- Information accurately related to driving voltage
- Load impedance inaccurately known



## Ideal characteristics



## Source termination impedance and load drive impedance



## Amplifier types

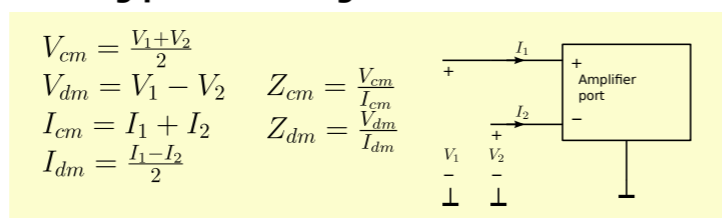
Follow from best source termination and load drive conditions for accurate signal transfer

signal transfer	Type	Z <sub>i</sub>	Z <sub>o</sub>	A	B	C	D
voltage amplifier	transadmittance	infinite	0	A	0	0	0
transimpedance	current amplifier	0	infinite	0	0	C	0
voltage to voltage / current	voltage amplifier	infinite	0	A	B	0	0
current to voltage / current	transadmittance	0	0	0	0	C	D
voltage / current to voltage	voltage amplifier	infinite	0	A	0	0	0
voltage / current to current	transimpedance	0	0	0	0	0	D
voltage / current to voltage / current	transadmittance	infinite	0	A	B	C	D

## Port isolation properties

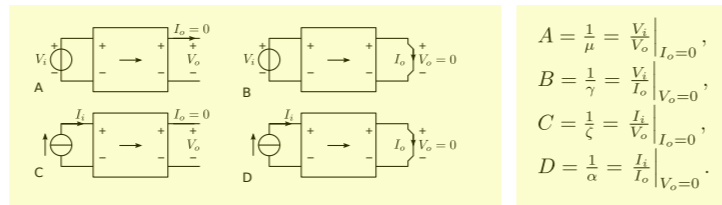
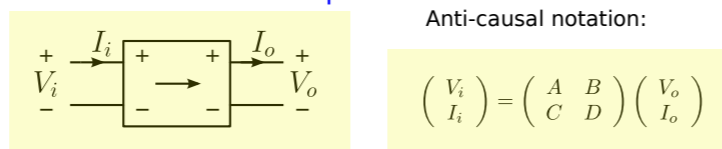
input-output	input-power	output-power	configuration
non-isolated	non-isolated	non-isolated	common-ground
non-isolated	non-isolated	isolated	x
non-isolated	isolated	non-isolated	differential receiver
non-isolated	isolated	isolated	floating supply
isolated	non-isolated	non-isolated	x
isolated	non-isolated	isolated	differential driver
isolated	isolated	non-isolated	x
isolated	isolated	isolated	differential receiver / driver

## Floating port modeling and characterization

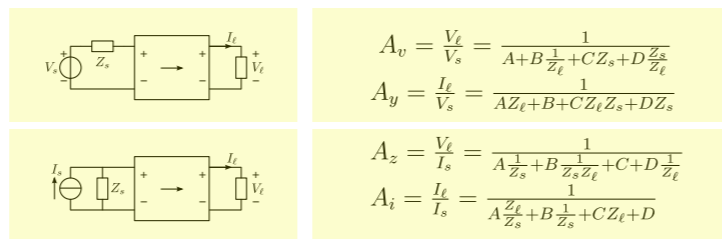


## Modeling of ideal behavior (natural two-port)

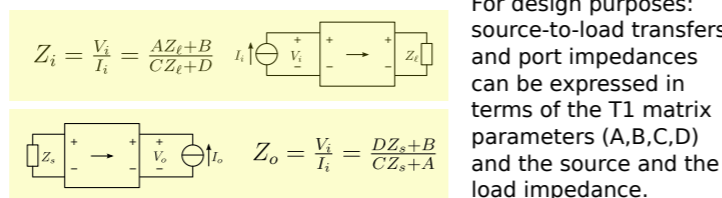
### Transmission-1 matrix representation



## Source-to-load transfer



## Port impedances



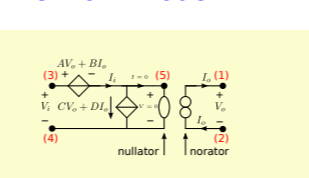
## Unilateral amplifier types

### Zero reverse transfer

$$AC = BD$$

Nullor and six non-unilateral types not listed in the table

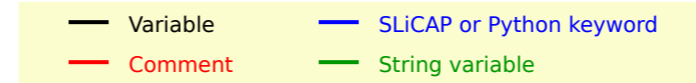
## Network model



## MNA matrix stamp

$$\begin{pmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{pmatrix} = \begin{pmatrix} 0 & 0 & 0 & 0 & 0 & -1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ C & -C & 0 & 0 & 0 & D & 1 \\ 0 & 0 & 0 & 0 & 0 & -D & 0 \\ 0 & 0 & 0 & 1 & -1 & 0 & 0 \\ -A & A & 1 & 0 & -1 & -B & 0 \end{pmatrix} \begin{pmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \\ V_5 \\ I_o \\ I_i \end{pmatrix}$$

## SLiCAP test bench for determination of the T1 matrix parameters



```
#!/usr/bin/env python3
# -*- coding: utf-8 -*-
"""
Python test bench for determination of the T1 parameters
- Use source identifier 'V1'
- Use load resistor value 'R_ell'
- Use the same file name for the .asc, .cir, .PNG, and .SVG
  files associated with a circuit.
"""
from SLiCAP import *

prj = initProject('ABCD-test')

# Create a list with circuit file names if you want to run multiple circuits.
fileNames = ['ABCD-test']
```

```
for fileName in fileNames:
    #makeNetlist(fileName + '.asc', fileName)

    # Define an instruction
    i1 = instruction()
    i1.setCircuit(fileName + '.cir')

    i1.setSimType('symbolic')
    i1.setGainType('gain')
    i1.setDataTypes('laplace')
```

```
# Use the same identifier for the source in all files
i1.setSource('V1')

# Define the detector for determination of the input voltage
i1.setDetector('V_i')
result = i1.execute()
# V_i is the transfer from V1 to the voltage V_i at node i
V_i = result.laplace
```

```
# Define the detector for determination of the input current
i1.setDetector('I_V1')
result = i1.execute()
# I_i is the transfer from V1 to the current through V1
I_i = -result.laplace

# Define the detector for determination of the output voltage
i1.setDetector('V_o')
result = i1.execute()
# V_o is the transfer from V1 to the output voltage
V_o = result.laplace
```

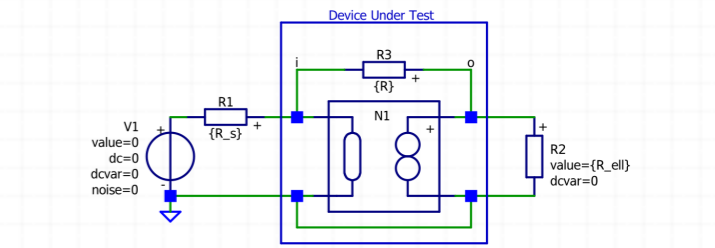
```
# Define the detector for determination of the output current
i1.setDetector('I_R2')
result = i1.execute()
# I_o is the transfer from V1 to the current through R2
I_o = result.laplace

# Calculate the T1 parameters
# Use the same name for the load resistance in all files
R_ell = sp.Symbol('R_ell')
A = sp.simplify(sp.limit(V_i/V_o, R_ell, 'oo'))
B = sp.simplify(sp.limit(V_i/I_o, R_ell, 0))
C = sp.simplify(sp.limit(I_i/V_o, R_ell, 'oo'))
D = sp.simplify(sp.limit(I_i/I_o, R_ell, 0))
```

```
htmlPage('Determination of T1 matrix parameters')
head2html('Test circuit')
img2html(fileName + '.svg', 400)
head2html('T1 matrix of the device under test')
text2html('The T1 matrix of the device under test is found as:')
T1 = sp.Matrix([[A,B], [C,D]])
eqn2html('T_1', T1)
Vili = sp.Matrix([[sp.Symbol('V_i')], [sp.Symbol('I_i')]])
Volo = sp.Matrix([[sp.Symbol('V_o')], [sp.Symbol('I_o')]])
# Display the matrix equation without execution of the multiplication
text2html('The matrix equation for the two-port (DUT) is found as:')
text2html('$\$' + sp.latex(Vili) + "= " + sp.latex(T1) + "\cdot" +
  sp.latex(Volo) + '$\$')
# Display the matrix equation with execution of the multiplication
text2html('This can be written as:')
eqn2html(Vili, T1*Volo)
```

## Determination of T1 matrix parameters

### Test circuit



### T1 matrix of the device under test

The T1 matrix of the device under test is found as:

$$T_1 = \begin{pmatrix} 0 & 0 \\ -\frac{1}{R} & 0 \end{pmatrix} \quad (1)$$

The matrix equation for the two-port (DUT) is found as:

$$\begin{pmatrix} V_i \\ I_i \end{pmatrix} = \begin{pmatrix} 0 & 0 \\ -\frac{1}{R} & 0 \end{pmatrix} \cdot \begin{pmatrix} V_o \\ I_o \end{pmatrix}$$

This can be written as:

$$\begin{pmatrix} V_i \\ I_i \end{pmatrix} = \begin{pmatrix} 0 \\ -\frac{V_o}{R} \end{pmatrix} \quad (2)$$

Go to ABCD-test\_index  
SLiCAP: Symbolic Linear Circuit Analysis Program, Version 1.1 © 2009-2022 SLiCAP development team  
For documentation, examples, support, updates and courses please visit: analog-electronics.eu  
Last project update: 2022-02-13 12:11:22

## Try yourself and verify the answer with SLiCAP

Assign symbolic values to the elements and determine the T1 transmission parameters by hand. Verify the obtained results with SLiCAP

